

4V Drive Pch MOSFET

RRL035P03

Structure

Silicon P-channel MOSFET

● Features

- 1) Low On-resistance.
- 2) High speed switching.

Application

Switching

Packaging specifications

	Package	Taping	
Type	Code	TR	
	Basic ordering unit (pieces)	3000	
RRL035P0	0		

● Absolute maximum ratings (Ta = 25°C)

Param	Symbol	Limits	Unit	
Drain-source voltage		V_{DSS}	-30	V
Gate-source voltage		V_{GSS}	±20	V
Drain current	Continuous	I_D	±3.5	Α
	Pulsed	I _{DP} *1	±14	Α
Source current (Body Diode)	Continuous	I _S	-0.8	Α
	Pulsed	I _{SP} *1	-14	Α
Power dissipation		P _D *2	1	W
Channel temperature		Tch	150	°C
Range of storage temperature		Tstg	-55 to +150	°C

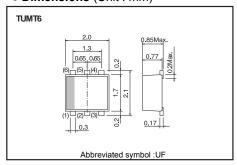
^{*1} Pw≤10µs, Duty cycle≤1%

• Thermal resistance

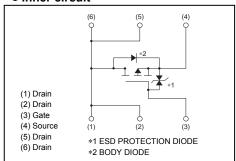
Parameter	Symbol	Limits	Unit
Channel to Ambient	Rth (ch-a)*	125	°C/W

^{*}Mounted on a ceramic board.

Dimensions (Unit : mm)



• Inner circuit



^{*2} Mounted on a ceramic board.

● Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I_{GSS}	1	-	±10	μA	V_{GS} =±20V, V_{DS} =0V
Drain-source breakdown voltage	$V_{(BR)DSS}$	-30	-	-	V	I _D =-1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	1	-	-1	μA	V _{DS} =-30V, V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	-1.0	1	-2.5	V	V_{DS} =-10V, I_{D} =-1mA
Static ducin course on state	*	ı	36	50		I _D =-3.5A, V _{GS} =-10V
Static drain-source on-state resistance	R _{DS (on)}	ı	52	72	mΩ	$I_D = -1.7A$, $V_{GS} = -4.5V$
		1	58	81		I _D =-1.7A, V _{GS} =-4.0V
Forward transfer admittance	IY _{fs} I*	2.3	-	-	S	I _D =-3.5A, V _{DS} =-10V
Input capacitance	C _{iss}	1	800	-	pF	V _{DS} =-10V
Output capacitance	C _{oss}	1	120	ı	pF	V _{GS} =0V
Reverse transfer capacitance	C _{rss}	-	110	-	pF	f=1MHz
Turn-on delay time	t _{d(on)} *	1	7	ı	ns	I _D =-1.7A, V _{DD} ≒-15V
Rise time	t _r *	-	9	-	ns	V _{GS} =-10V
Turn-off delay time	t _{d(off)} *	1	75	-	ns	R_L =8.8 Ω
Fall time	t _f *	1	40	ı	ns	R_G =10 Ω
Total gate charge	Q _g *	-	8.0	-	nC	I _D =-3.5A, V _{DD} ≒-15V
Gate-source charge	Q _{gs} *	-	2.5	-	nC	V_{GS} =-5V R _L =4.3 Ω
Gate-drain charge	Q _{gd} *	-	3.0	-	nC	R_G =10 Ω

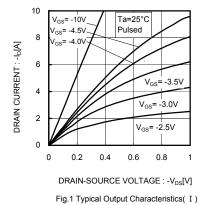
^{*}Pulsed

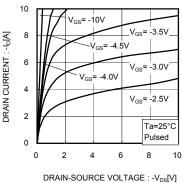
●Body diode characteristics (Source-Drain) (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward Voltage	V _{SD} *	-	-	-1.2	V	I _s =-3.5A, V _{GS} =0V

^{*}Pulsed

Electrical characteristics curves





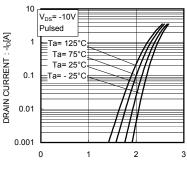
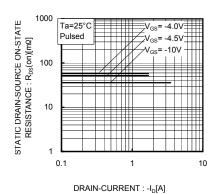


Fig.2 Typical Output Characteristics(II)





1000 V_{cs}= -10V Ta=125°C Ta=125°C Ta=25°C Ta=

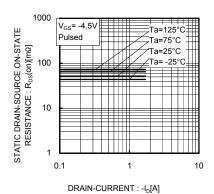
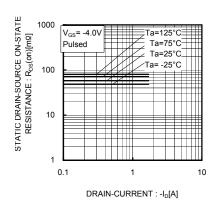
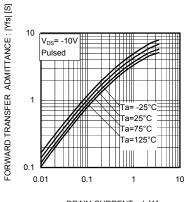


Fig.4 Static Drain-Source On-State
Resistance vs. Drain Current(I)

Fig.5 Static Drain-Source On-State
Resistance vs. Drain Current(II)

Fig.6 Static Drain-Source On-State
Resistance vs. Drain Current(III)





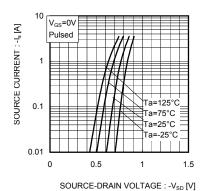
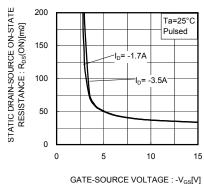


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current(IV)

DRAIN-CURRENT : -I_D[A]
Fig.8 Forward Transfer Admittance
vs. Drain Current

Fig.9 Reverse Drain Current vs. Sourse-Drain Voltage



GATE-SOURCE VOLTAGE: -V_{GS}[V]

Fig.10 Static Drain-Source On-State
Resistance vs. Gate Source Voltage

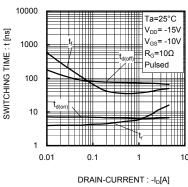


Fig.11 Switching Characteristics

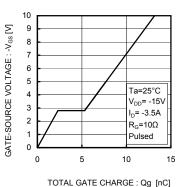
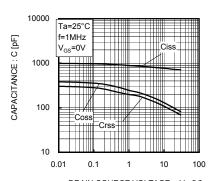


Fig.12 Dynamic Input Characteristics



DRAIN-SOURCE VOLTAGE : -V_{DS}[V]
Fig.13 Typical Capacitance
vs. Drain-Source Voltage

Measurement circuits

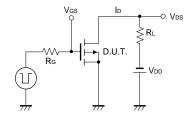


Fig.1-1 Switching Time Measurement Circuit

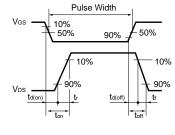


Fig.1-2 Switching Waveforms

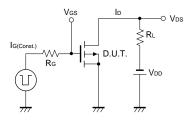


Fig.2-1 Gate Charge Measurement Circuit

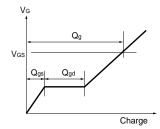


Fig.2-2 Gate Charge Waveform

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