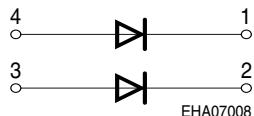
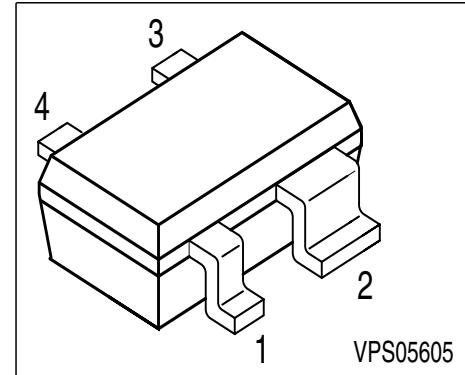


Silicon Schottky Diode**Preliminary data**

- Low barrier diode for detectors up to GHz frequencies
- For high-speed switching applications
- Zero bias detector diode



ESD: Electrostatic discharge sensitive device, observe handling precaution!

Type	Marking	Pin Configuration				Package
BAT 63-07W	63s	1=C1	2=C2	3=A2	4=A1	SOT-343

Maximum Ratings

Parameter	Symbol	Value	Unit
Diode reverse voltage	V_R	3	V
Forward current	I_F	100	mA
Total power dissipation, $T_S = \text{tbd}$ °C	P_{tot}	100	mW
Junction temperature	T_j	150	°C
Storage temperature	T_{stg}	-55 ... 150	

Thermal Resistance

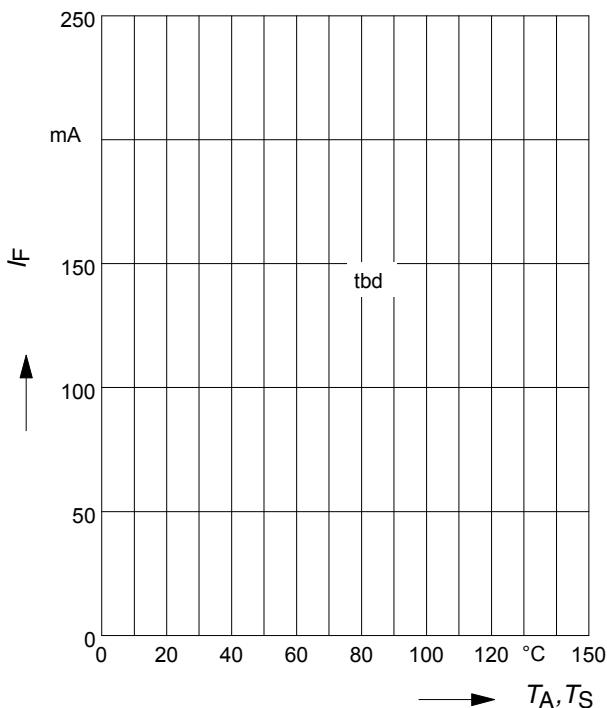
Junction - ambient 1)	R_{thJA}	$\leq \text{tbd}$	K/W
Junction - soldering point	R_{thJS}	$\leq \text{tbd}$	

Electrical Characteristics at $T_A = 25^\circ\text{C}$, unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC characteristics					
Reverse current $V_R = 3 \text{ V}$	I_R	-	-	10	μA
Forward voltage $I_F = 1 \text{ mA}$	V_F	-	190	300	mV
AC characteristics					
Diode capacitance $V_R = 0.2 \text{ V}, f = 1 \text{ MHz}$	C_T	-	0.65	0.85	pF
Case capacitance $f = 1 \text{ MHz}$	C_C	-	0.1	-	
Differential resistance $V_R = 0 \text{ V}, f = 10 \text{ kHz}$	R_0	-	30	-	$\text{k}\Omega$
Series inductance	L_s	-	1.6	-	nH

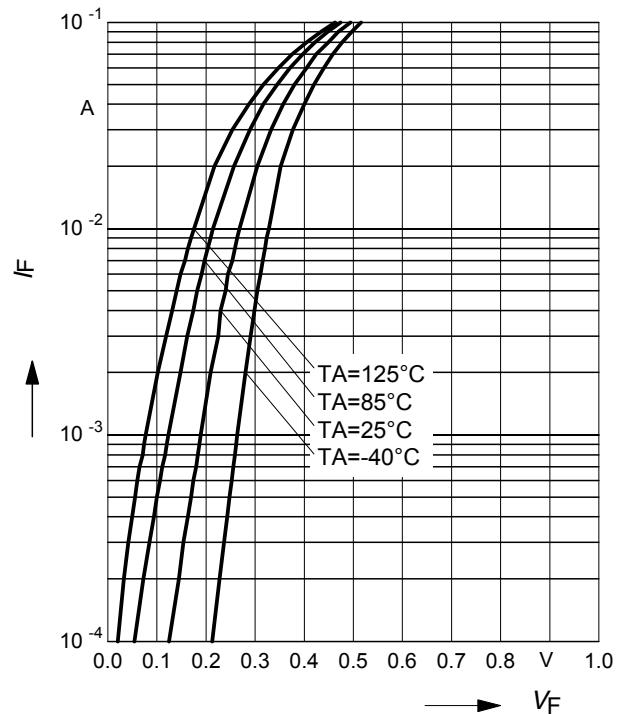
Forward current $I_F = f(T_A^*; T_S)$

* mounted on alumina



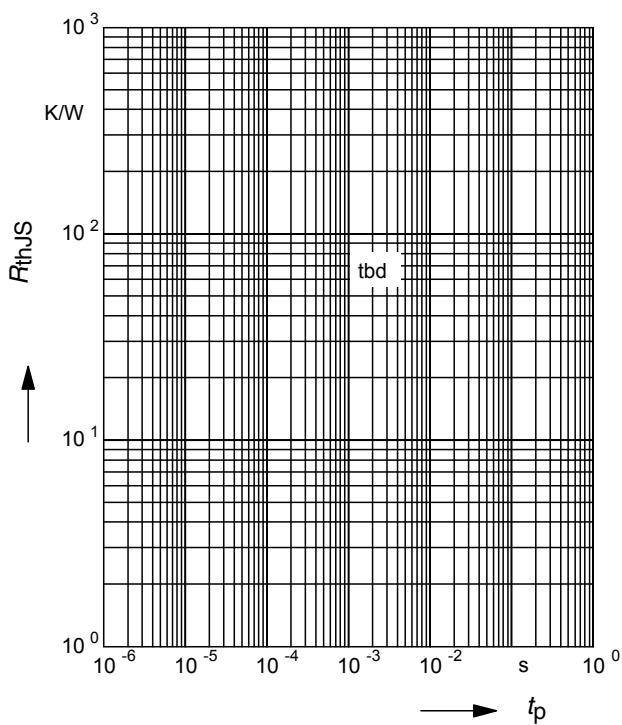
Forward current $I_F = f(V_F)$

T_A = parameter

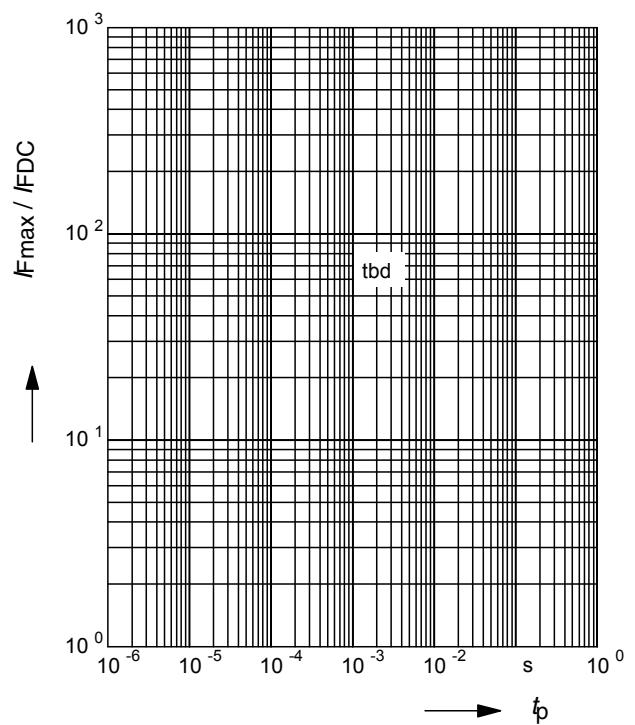


Permissible Pulse Load

$I_{F\max} / I_{FDC} = f(t_p)$

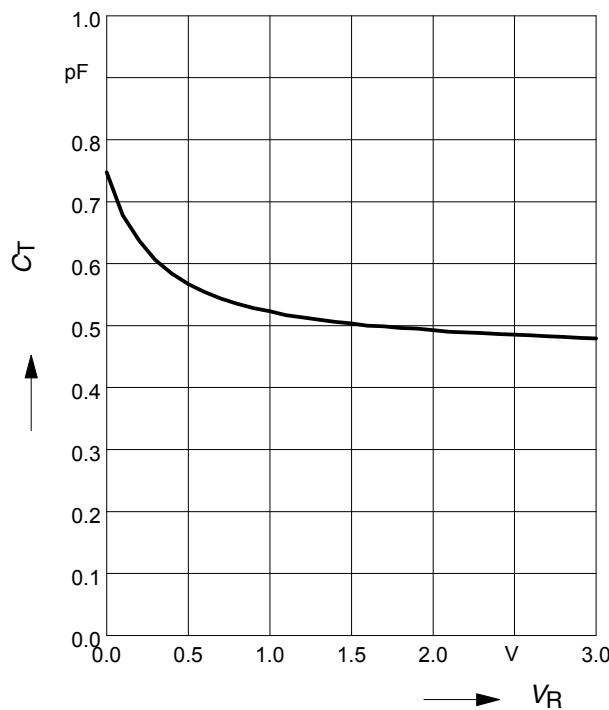


Permissible pulse load $I_{F\max} / I_{FDC} = f(t_p)$



Diode capacitance $C_T = f(V_R)$

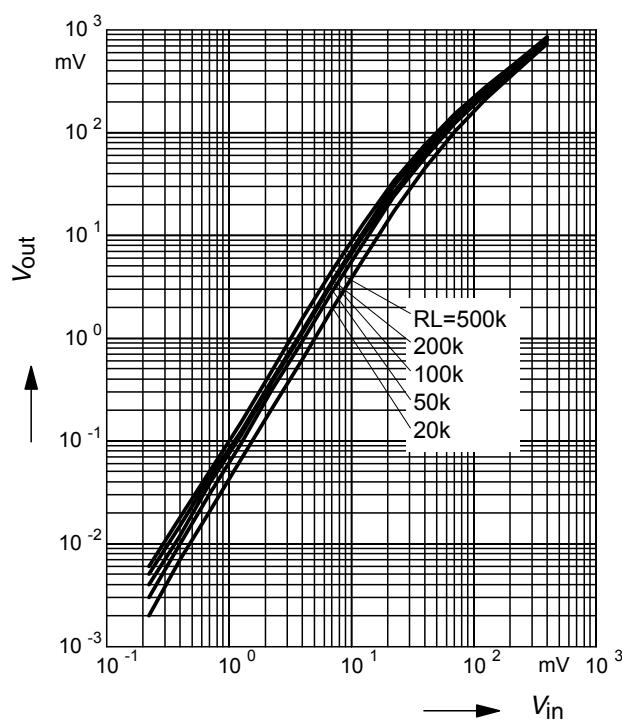
$f = 1\text{MHz}$



Rectifier voltage $V_{out} = f(V_{in})$

$f = 900\text{ MHz}$

R_L = parameter in k Ω



Testcircuit:

